

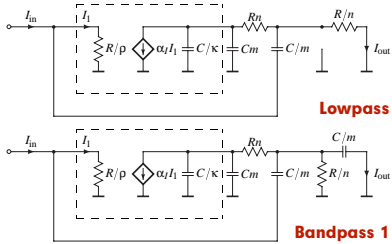
Fundamental Frequency Limitations in Current-Mode Sallen-Key Filters

Hanspeter Schmid and George S. Moschytz, Signal and Information Processing Lab., ETH Zürich, Switzerland. hanspeter.schmid@isi.ee.ethz.ch

Summary

We investigated high-frequency current amplifier non-idealities and their effects on all-pole Sallen-Key filter biquads. It can be shown that **current amplifier non-idealities** cause parasitic zeros in the filter transfer functions, and thus **impose fundamental limitations on the realizable pole frequency**. Design equations are given, providing compensation for the amplifier's port impedances and phase lag, by predistortion of the component values. It is also shown how design specifications for a current amplifier can be derived from the filter specification, minimizing the amplifier's power dissipation. Finally, a design procedure is discussed by the example of a video-frequency lowpass filter biquad.

Current-Mode Sallen-Key Filters



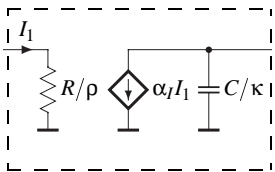
Both a **Bandpass 2** and a **Highpass** filter are easily derived by exchanging Rs and Cs in the Bandpass 1 and Lowpass filter, respectively. Then, assuming an ideal amplifier,

$$\left(\omega_{p1}, \frac{1}{q_{p1}}\right) = \left(\frac{1}{RC}, \frac{m^2 n^2 + m^2 + (\alpha_I + 1)}{mn}\right), \quad (2LP)$$

$$\left(\frac{1}{\sqrt{2}RC}, \frac{m^2 n^2 + m^2 + (\alpha_I + 2)}{\sqrt{2}mn}\right), \quad (2BP1)$$

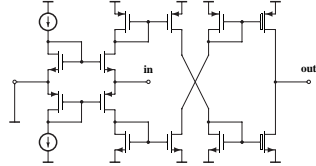
with similar expressions for BP2 and HP.

Current Amplifier Model



ρ and κ describe the port impedances in relation to the passive part's R and C . Note: $\alpha_I < 0$ in Sallen-Key filters, because the VCVS and CCCS definitions are *not* dual.

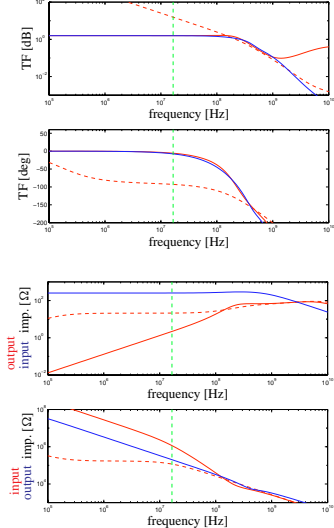
Open-Loop Current Amplifier



This schematic shows the current amplifier used for the design example (it is also called *second generation current conveyor, CCI*). The transistor symbols with the "boxed" gates indicate super transistors. We used regulated cascodes in our example. The simulated values at 16.5 MHz:

$$\alpha_I = -1.57, C_{out} = 0.048 \text{ pF}, R_{in} = 254 \Omega, \phi = 7.65^\circ$$

Open-Loop vs Closed-Loop Amp



These figures show the transfer function and the input and output impedances of

blue: A CCII used as a current buffer
red: An opamp (AD SSM 2135) connected as a buffer
red dashed: dito, open-loop

Note: the opamp is actually 50 times slower, the red curves are scaled in frequency ($\times 50$) and magnitude ($\times 1.57$).

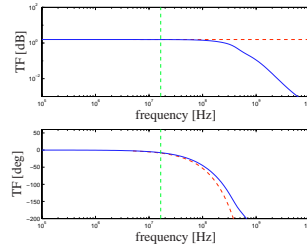
Towards the frequency limits, the advantages of the closed-loop amplifiers vanish together with the loop gain.

Phase Model for the Amplifier

The current amplifier does not have one *dominant* pole. It can be seen that a model in which the phase lag is linearized through the points at $\omega = 0$ and $\omega = \omega_p$ results in a better fit of model and "reality" than a one-pole model. Thus

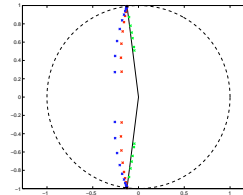
$$\alpha_I(s) = \alpha_{I_{DC}} \cdot e^{-\phi \frac{s}{\omega_p}}$$

where ϕ is the phase lag at the filter's pole frequency ω_p . The figure below shows the **simulated** and the **analytically modeled** transfer function of the buffer. Green: ω_p .



Pole Shift (Lowpass Filter)

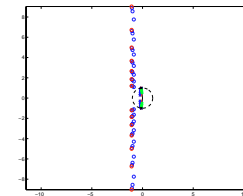
This figure shows the poles in the normalized s -plane. The circle is the location of constant frequencies ($\omega_p = 1$), the lines are the locations of constant pole quality ($q_p = 4$).



blue: $\kappa = 1.0, 1.8, 3.2, 5.6, 10, 18, 32, 56, 100, \dots$
red: $\rho = 1.0, 1.8, 3.2, 5.6, 10, 18, 32, 56, 100, \dots$
green: $\phi = 3, 6, 9, 12, 18, 21^\circ$

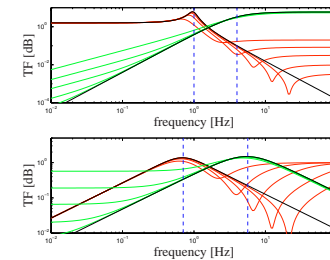
Parasitic Zeros (Lowpass Filter)

The following figure is the same as above, in a larger scale, showing the parasitic zeros for some values of ρ and κ .



red: $\rho = 1.0, 1.8, 3.2, 5.6, 10.0, 17.8, 31.6, \kappa \rightarrow \infty$
blue: ρ as above, $\kappa = 10, 33, 100$

Transfer Functions ($\kappa = 30, \rho = 3 \dots 300$)



These plots show the normalized transfer functions of the **Lowpass, Bandpass 1, Bandpass 2** and **Highpass** filters for constant κ and different values of ρ .

Frequency Limitations (LP)

The maximum possible ω_p depends on the component spread and the stopband attenuation:

$$\omega_{p \max} \approx \frac{1}{10 \cdot \max(m, 1/m) C_o \cdot \max(n, 1/n) R_I \cdot A_{\text{stop}}}$$

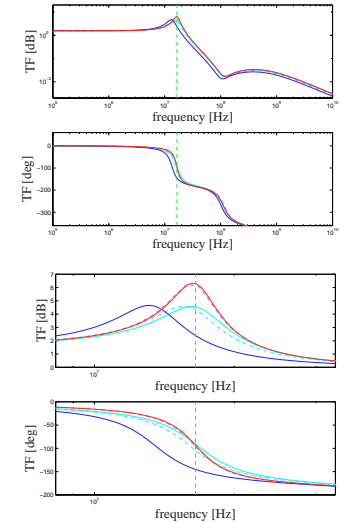
$$\text{Design: } C_o R_I \leq \frac{1}{\omega_p} \cdot \frac{1}{10 \cdot \max(m, 1/m) \cdot \max(n, 1/n) \cdot A_{\text{stop}}}$$

Design Procedure and Example

- $f_p = 16.5 \text{ MHz}, q_p = 4, A_{\text{stop}} = 30 \text{ dB}, m = 0.6, n = 1.$
- (1) blue: $C \approx 20 \cdot C_{out} = 0.96 \text{ pF}, R = 10.04 \text{ k}\Omega$
 - (2) cyan dashed: R corrected by $(3LP)$: $R = 8.20 \text{ k}\Omega$
 - (3) cyan: $1/R$ linearly extrapolated: $R = 7.81 \text{ k}\Omega$
 - (4) magenta dashed: m corrected by $(4LP)$: $m = 0.586$
 - (5) red: m linearly extrapolated: $m = 0.566$.

Simulated values (BSIM 3v3, 0.6 μm CMOS process):

#	f_p [MHz]	q_p	Δf_p	Δq_p
(1)	13.39	2.98	-19%	-26%
(2)	15.83	2.91	-4%	-27%
(3)	16.46	2.91	$\approx 0\%$	-27%
(4)	16.40	3.95	-0.5%	-1.3%
(5)	16.40	4.01	-0.5%	$\approx 0\%$



Acknowledgements

We would like to thank D. Lim and M. Helfenstein for their valuable comments on our work.